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Subject: [NSCI] Fwd: 2017 IEEE International Conference on Rebooting Computing-Call for Papers is Now Open!
Date: Tuesday, April 11, 2017 11:59:39 AM
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FYI

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Subject: 2017 IEEE International Conference on Rebooting Computing-Call for Papers is Now Open!
Date: April 11, 2017 at 10:35:12 AM EDT
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2017 IEEE International Conference on Rebooting Computing

8-9 November 2017
Washington, DC, USA

Overview: The goal of the [2nd IEEE International Conference on Rebooting Computing](#) is to discover and foster novel methodologies to reinvent computing technology, including new materials and physics, devices and circuits, system and network architectures, and algorithms and software. The conference seeks input from a broad technical community, with emphasis on all aspects of the computing stack.

Program Highlights: Plenary Speakers, paper presentations, poster sessions, and Panel session

Plenary Speakers:

- Prof. Luis Ceze, University of Washington

Location

[The Ritz-Carlton Hotel](#)
1700 Tysons Boulevard
McLean, VA, USA
(Just outside Washington, DC)

Important Dates

- 26 May 2017
Paper abstracts due
- 9 June 2017
Short and full paper submissions due
- 22 July 2017
Acceptance notification of papers
- 22 August 2017
Final paper submissions and poster abstracts due

- Prof. Margaret Martonosi, Princeton University
- Prof. Karlheinz Meier, University of Heidelberg
 - Prof. Robert Schoelkopf, Yale Quantum Institute

Topics of Interest: We [invite submissions](#) related to fundamentally new ways to compute.

Topics include, but are not limited to:

- Neuromorphic architectures
- Brain-inspired computing
- Approximate and stochastic computing
- Non-Von-Neumann computer architectures, including in-memory processing and memory-based computing
- Quantum computing
- Optical computing
- Biological computing
- Extending Moore's law and augmenting CMOS
- Novel device physics and materials including spin-based electronics
- Energy-efficient computing including reversible, adiabatic, and ballistic computing
- Superconducting and cryogenic computing
- Error-tolerant logic and circuits
- Exploiting nonlinear dynamics and chaos in device behavior
- Cellular automata and cellular neural networks
- Beyond von Neumann software issues, including operating systems, compilers, security, and resource management
- Future of design automation

Submission Information

Authors may submit either full papers or short papers following the Author guidelines. Author guidelines will be published soon.

For more information, please email rcinfo@ieee.org.

- 2 September 2017
Acceptance notification of posters
- 29 September 2017
Early registration deadline

Co-Located Events

- [International Roadmap for Devices and Systems](#) (IRDS) progress summaries
Tuesday, 7 November 2017
- Industry Summit following ICRC 2017
Friday, 10 November 2017

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